

CURRICULLUM VITAE

MITHA RAJ

M.Tech(Applied Electronics and Communication Engineering)



Contact No:

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Residential address:

Mitha Raj

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Permanent Address:

Mitha Raj

D/o Devarajan A

Allipra(H)
Karakkode P.O
Manimooly
Malappuram DT pin-679333
Kerala, India

Personal Details:

Mother's Name: Rajeswari
Father's Name: Devarajan A
Date of Birth: 30 June 1992
Sex: Female
Nationality: Indian
Marital Status: Married
Visa Details: Husband's sponsorship

OBJECTIVE

To work in a challenging and dynamic business environment and to keep adding value to the organization that I represent and to serve myself, while concurrently upgrading my skills and knowledge in the respective functional department of the organization.

STRENGTHS

Concentration with a goal.
Positive and Supportive.
Adaptability to any working environment.
Managing multiple Priorities.
Risk taker
Strong learner and innovative

EDUCATION

PROFESSIONAL QUALIFICATIONS:

EDUCATION	COLLEGE/ INSTITUTION	GRADE POINTS / % MARKS	YEAR OF PASSING
M.Tech (Applied Electronics and Communication Systems)	Cochin college of engineering and technology, Valanchery	7.8	June 2017
B.Tech (Electronics and communication engineering)	Palakkad Institute of Science and technology, Palakkad	6.57	April. 2014
Plus Two	EMORGHSS Palad	85%	2010
Secondary school leaving	CKHS Manimooly	85%	2008

AREA OF INTRESTS

Telecommunication
Digital Electronics
Networking

Languages Known:

English
Malayalam
Hindi

Key traits:

Self-motivated
Honesty/Morality
Dedication
Flexibility
Energetic

Interests:

Music & craft
Travelling
Reading

CORE SKILLS

Deep knowledge in Electronics and Communication,
Leadership and discipline.

Excellent Computer/Technical literacy. (MS Office, Adobe
Photoshop, MAT Lab (beginner), VHDL Verilog.)

ACADEMIC PROJECT

A HIGH SPEED RSD BASED ECC PROCESSOR USING TOOM
COOK AND KARATSUBA ALGORITHM.

Security is the major factor in every communication. The
project is based on elliptic curve cryptographic processor.

ACHIEVEMENTS

Secured Second position in construction in pure
mathematics competition held at district level.

Won many proficiency prizes in schools.
Paper publication

A Comparative study of RSD based ECC processor
using Karatsuba and Toom Cook Algorithm (IJARIIT).

Attended PCB Design workshop held at
Government Engineering College Kozhikode

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Declaration

I hereby declare that the above-mentioned information is correct
up to my knowledge and I bear the responsibility for the correctness
of the above-mentioned particulars.

Place: Dubai
Date:

Mitha Raj